

Multi-Layer CCD

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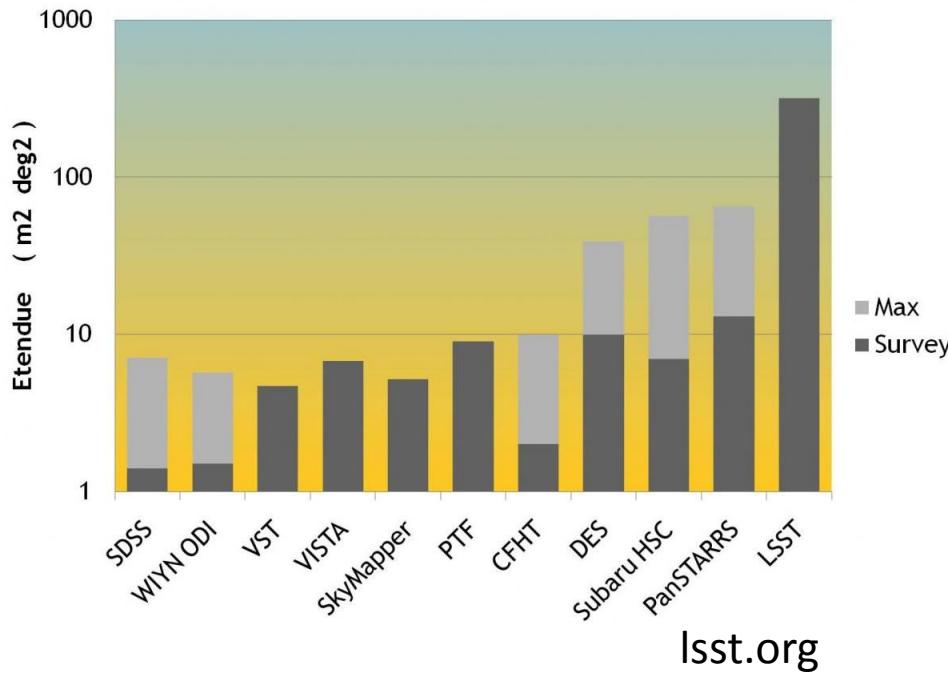


Outline

- Telescope efficiency
- Multi-layer sensors
- Our approach

Telescope efficiency

- Expensive facilities, lengthy surveys
- Speed \sim étendue = aperture area \times FOV



Telescope efficiency

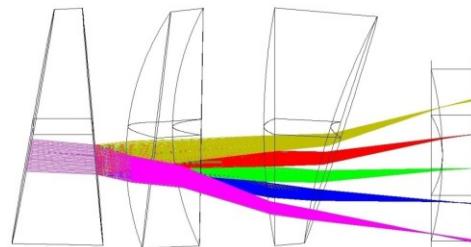
- Color imaging



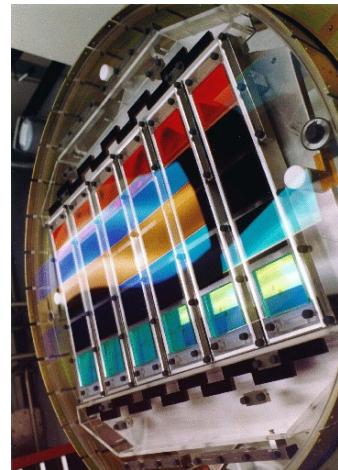
JWST NIRCAM



HST UVIS



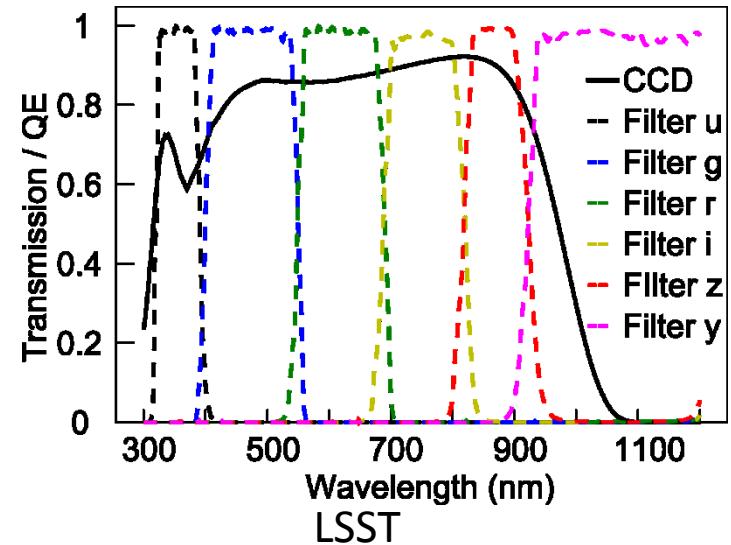
Swift UVOT



SDSS



LSST



Telescope efficiency

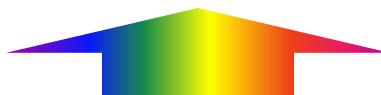
- Color acquisition limits efficiency.



Monochromatic pixels



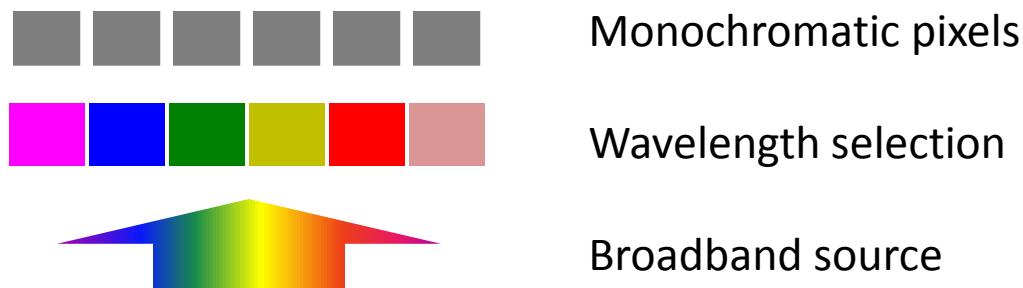
Wavelength selection



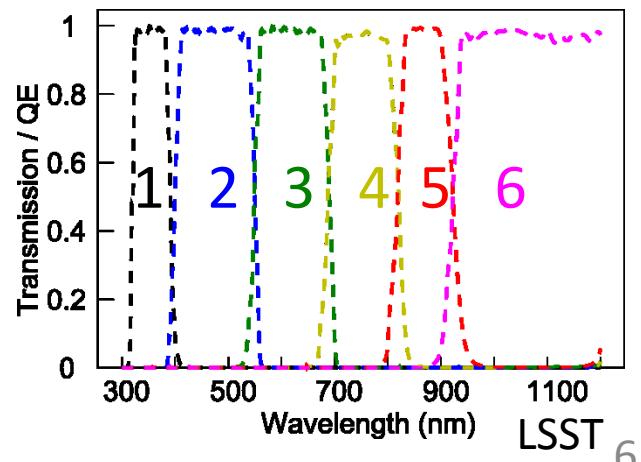
Broadband source

Telescope efficiency

- Color acquisition limits efficiency.



- How much is lost?
 - # of colors = N
 - Wasted photons (%) = $\frac{N-1}{N}$
 - Room for improvement = N



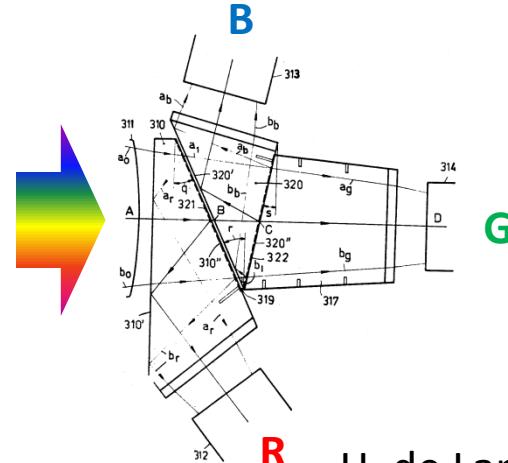
Telescope efficiency

- To utilize all incident photons

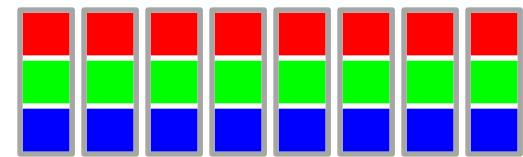
- Multiple sensors

- Hard to include more colors (multiple prisms, CCDs)
 - Bulky, expensive

- Polychromatic pixels



H. de Lang 1965



More info per pixel

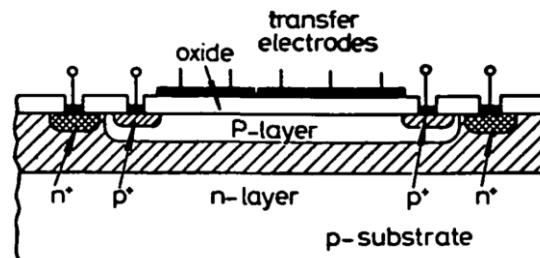
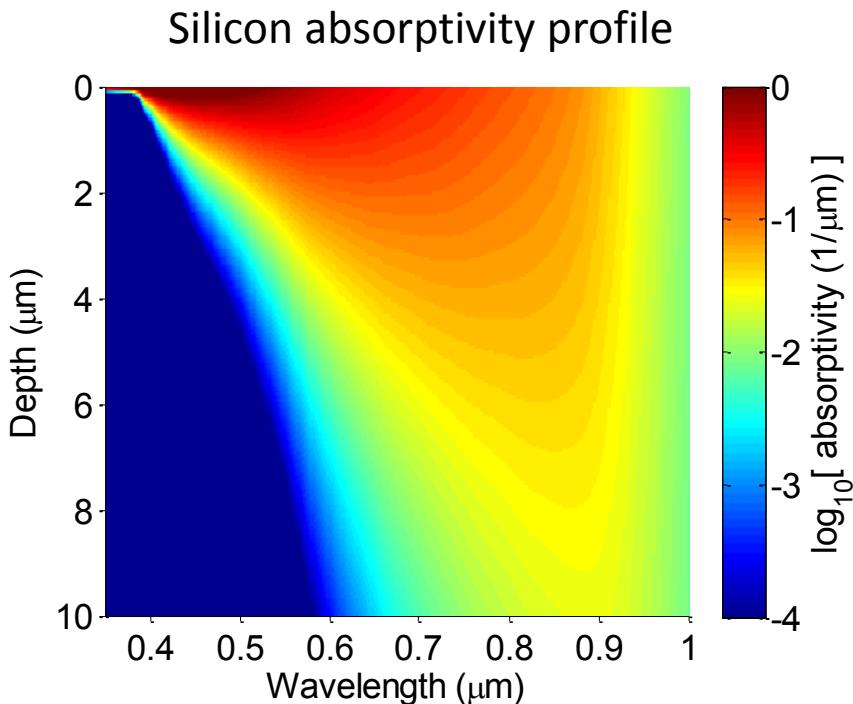


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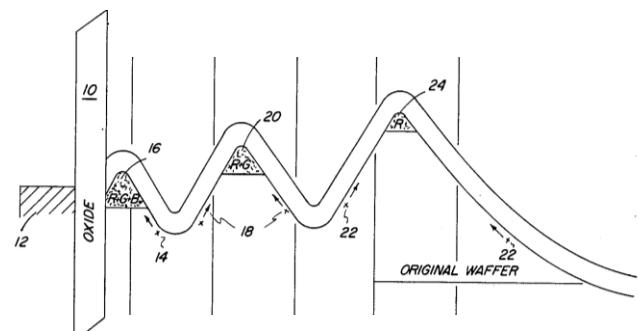
- Telescope efficiency
- Multi-layer sensors
- Our approach

Multi-layer sensors

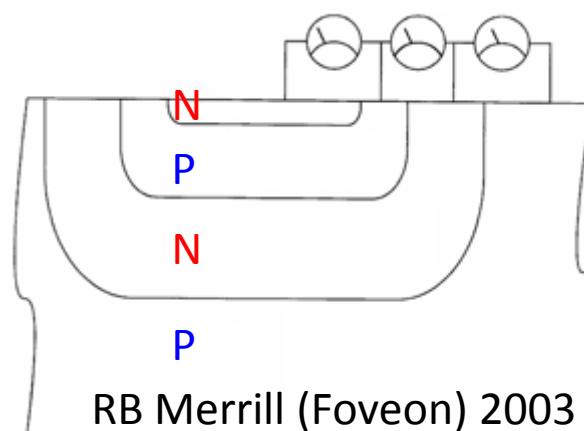
- Colors in the substrate



RM Barsan, 1978



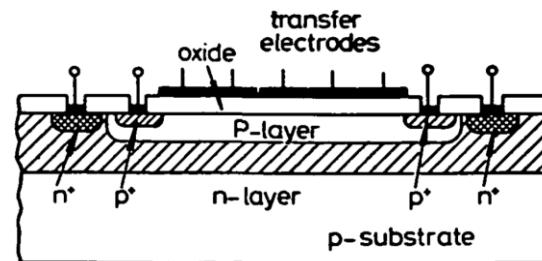
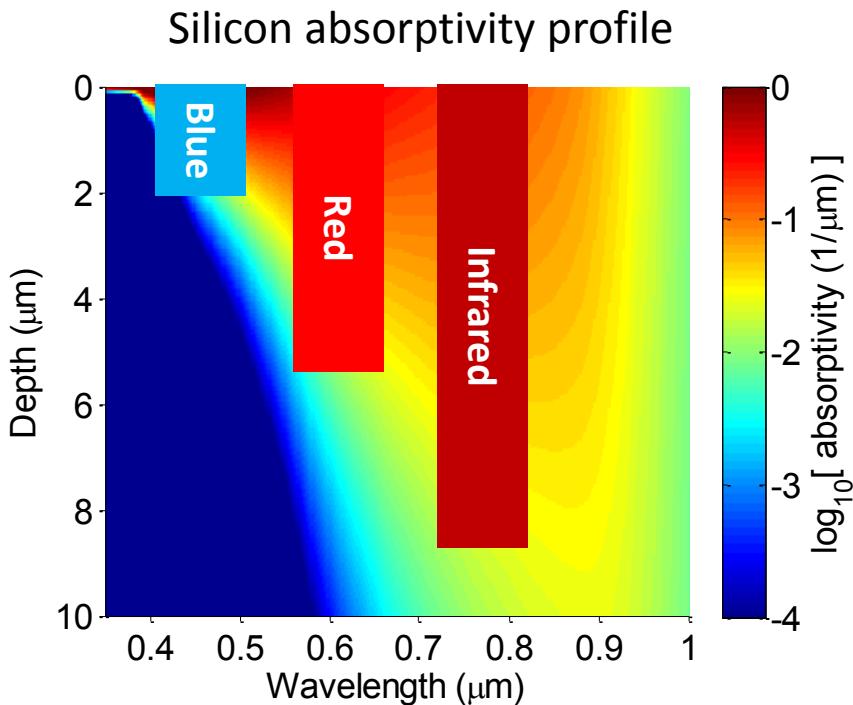
BC Burkey (Kodak) 1986



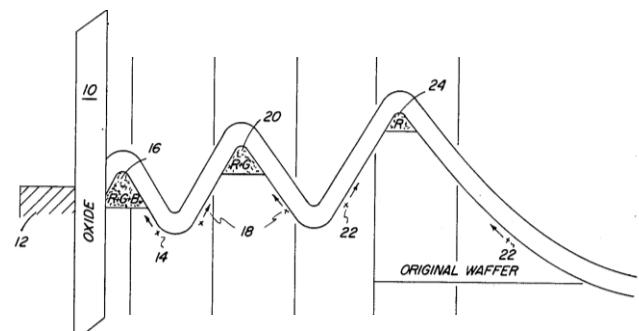
RB Merrill (Foveon) 2003

Multi-layer sensors

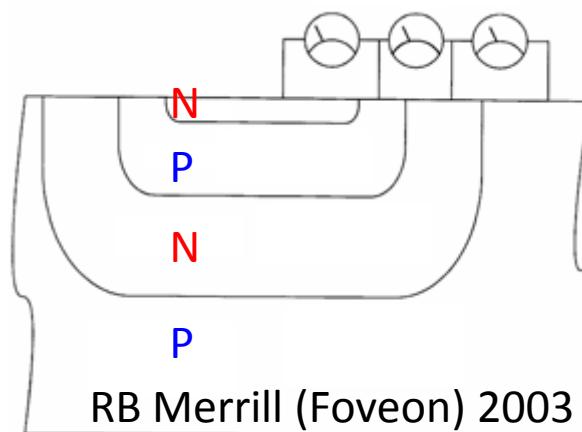
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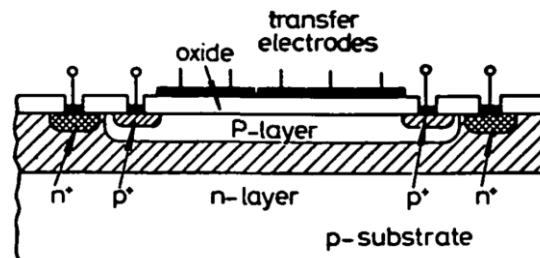
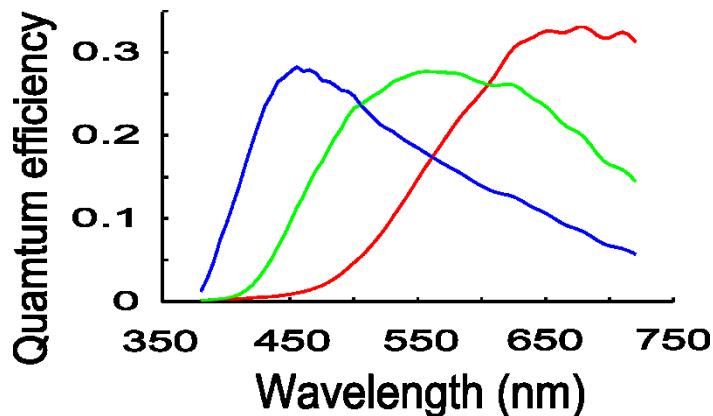
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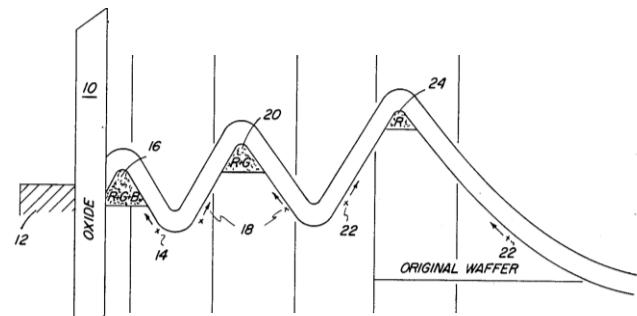
RB Merrill (Foveon) 2003

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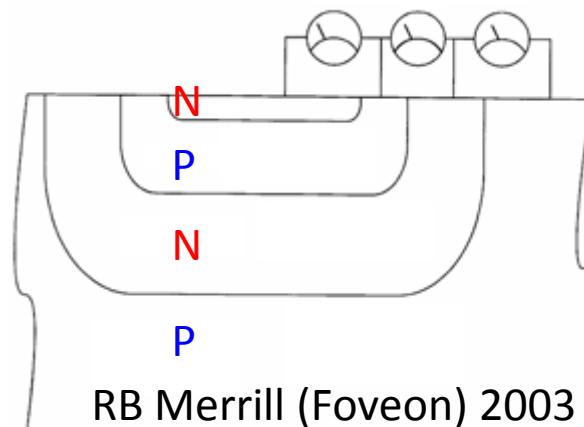
- PN-junction layers
 - Color crosstalk



RM Barsan, 1978



BC Burkey (Kodak) 1986



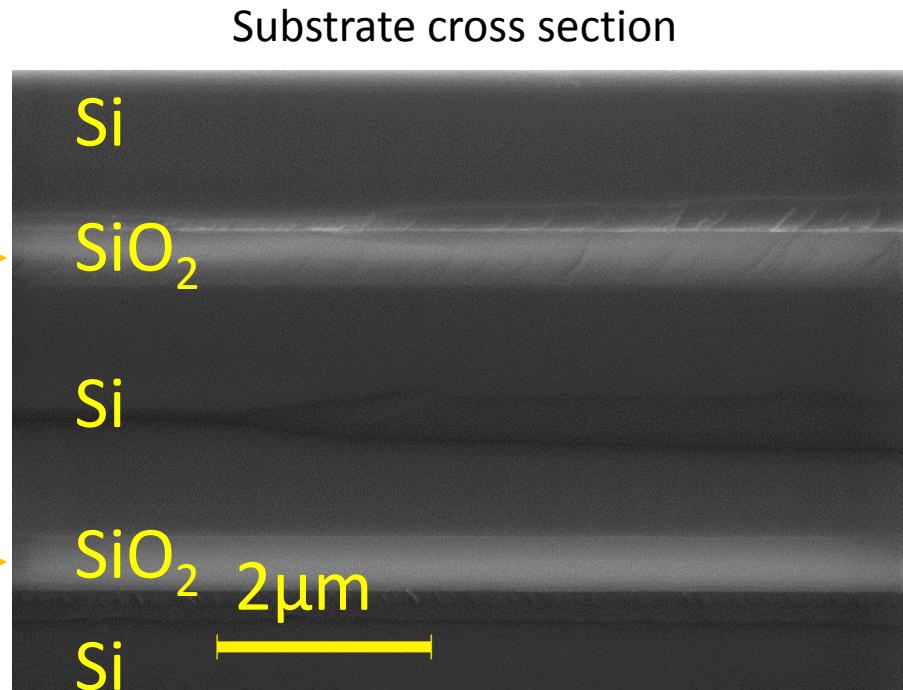
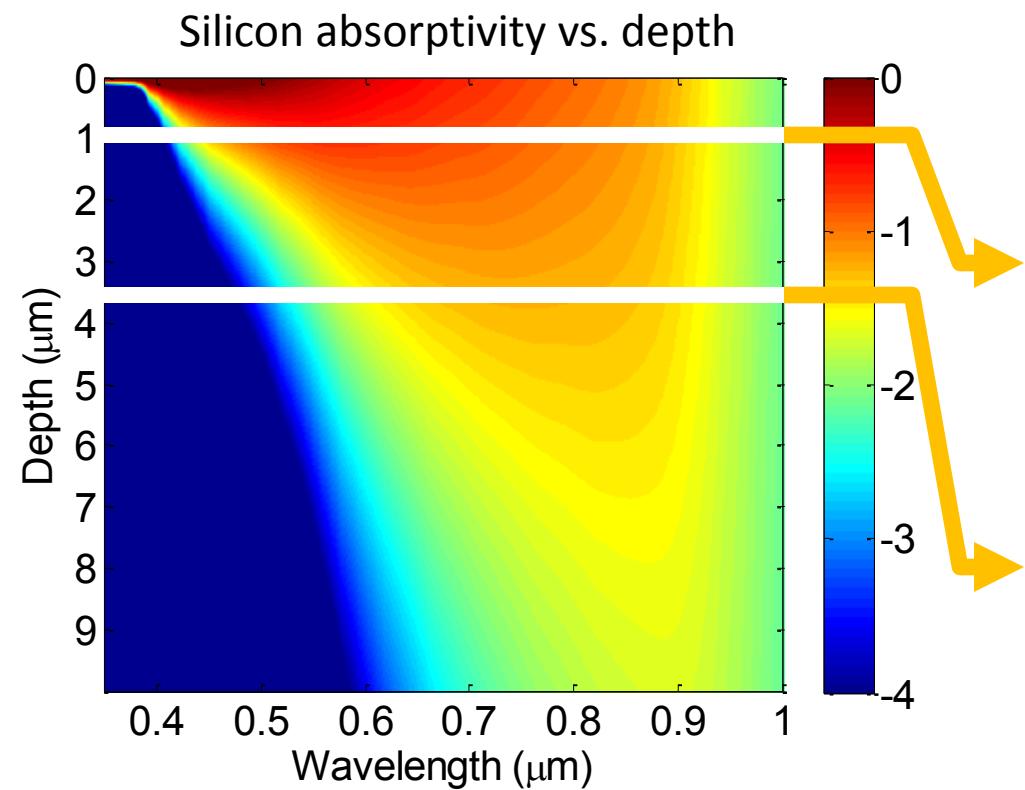
RB Merrill (Foveon) 2003

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- Our approach
 - Concept
 - Design
 - Experiment

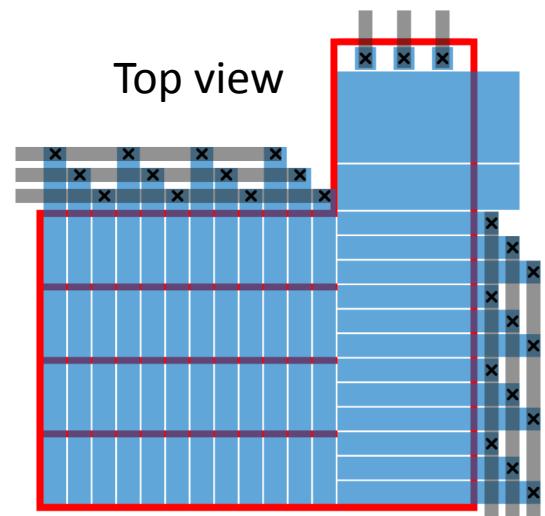
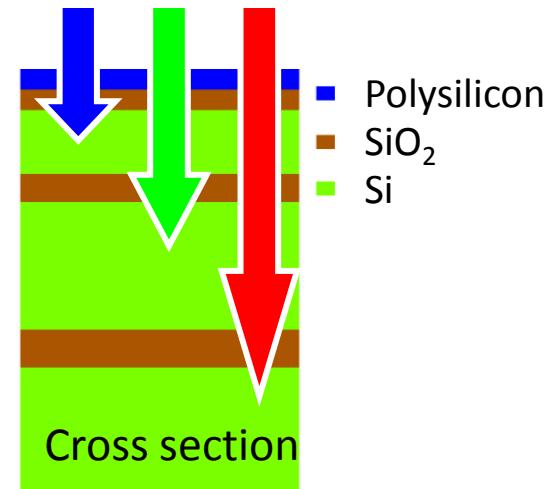
Multi-layer SOI CCD

- CCD on silicon-on-insulator (SOI) substrates
 - Each Si layer provides a color.



Multi-layer SOI CCD

- Features
 - Minimal waste of photons
 - Buried insulators
 - Optical/electrical separation
 - No in-pixel connections
 - Can stack more colors/layers
- CCD + buried optical filters
→ Multifold faster color imaging

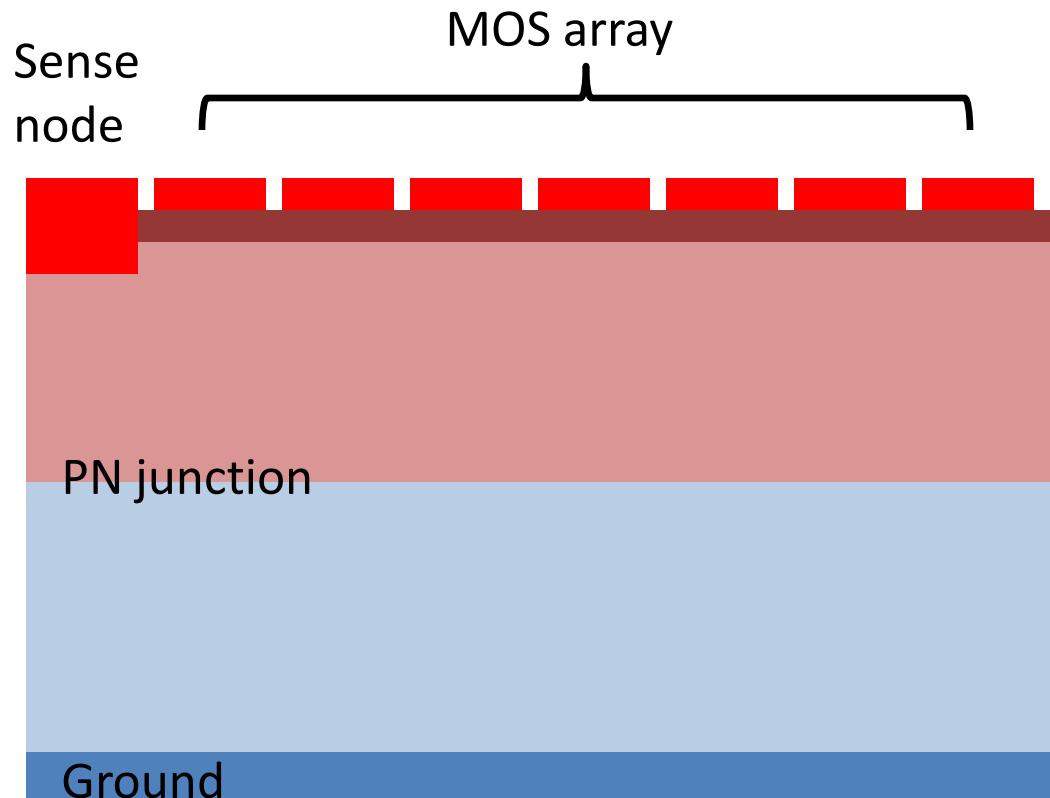


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- Multi-layer SOI CCD
 - Concept
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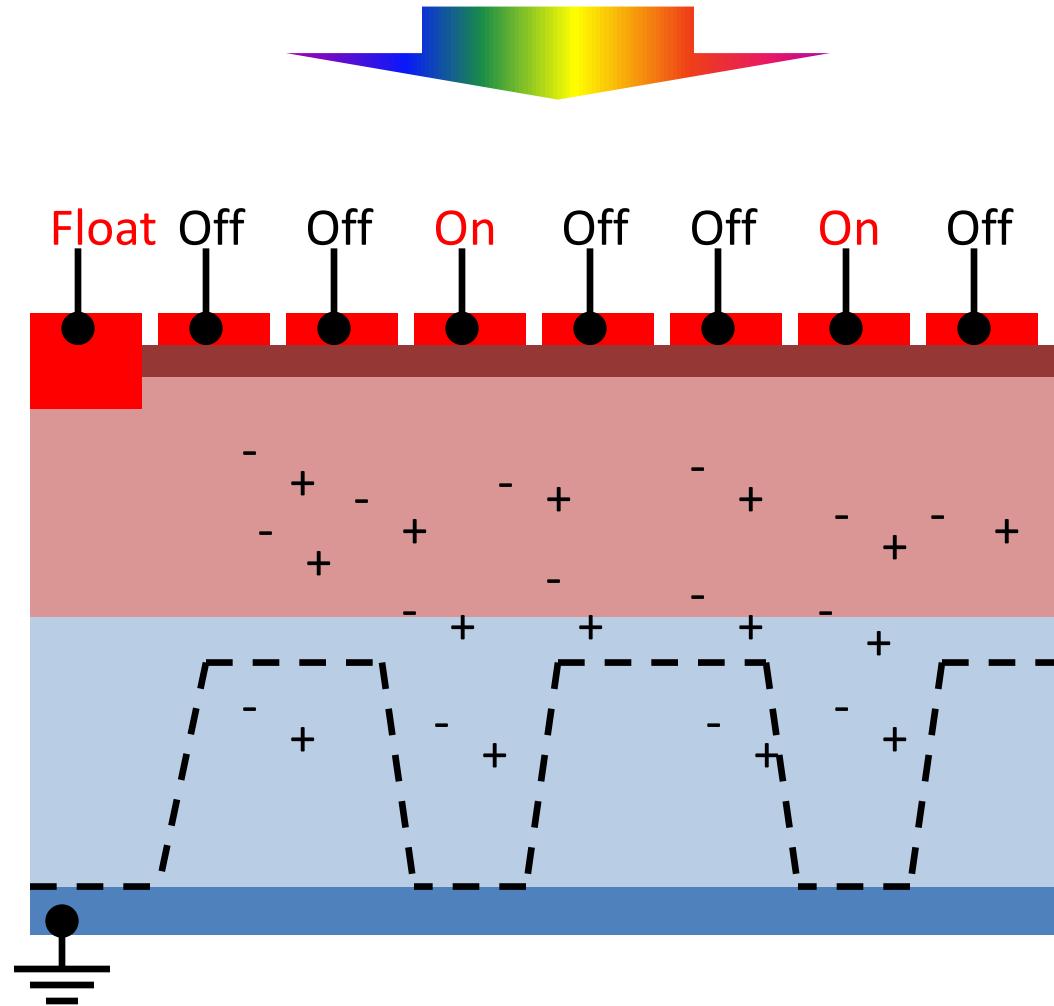
CCD quick review

- Device operation
 - Equilibrium
 - Channel depletion
 - Optical absorption
 - Charge generation
 - Charge collection
 - Charge transfer
 - Charge measurement



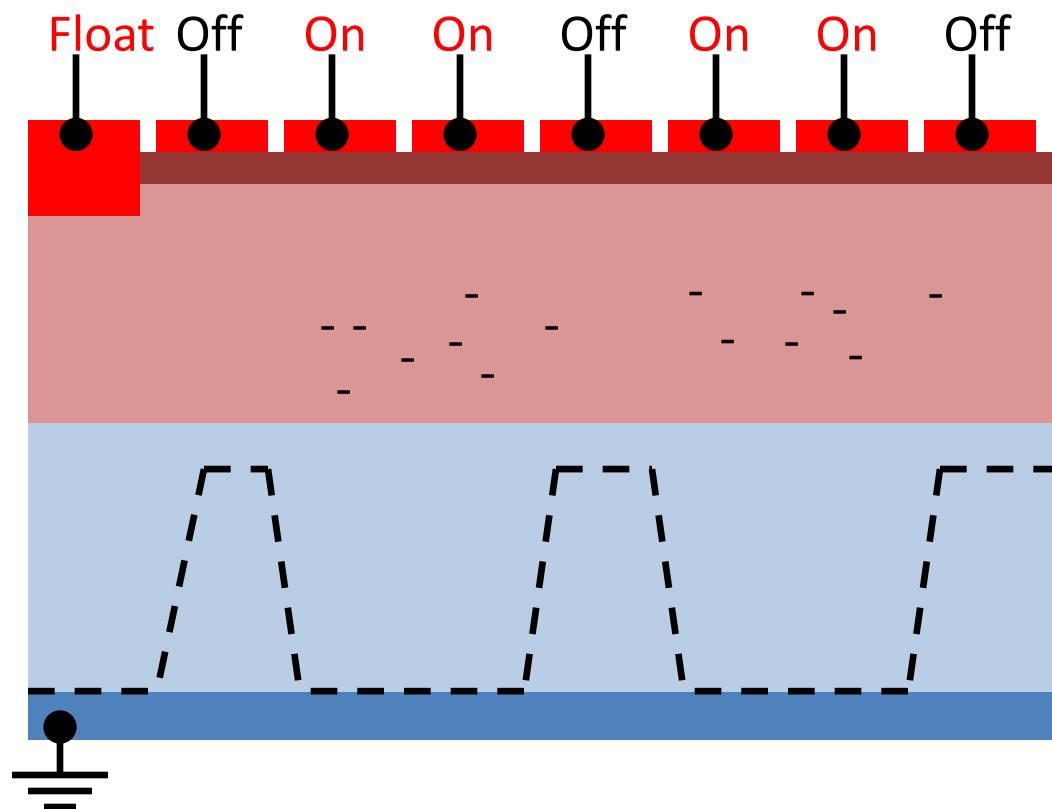
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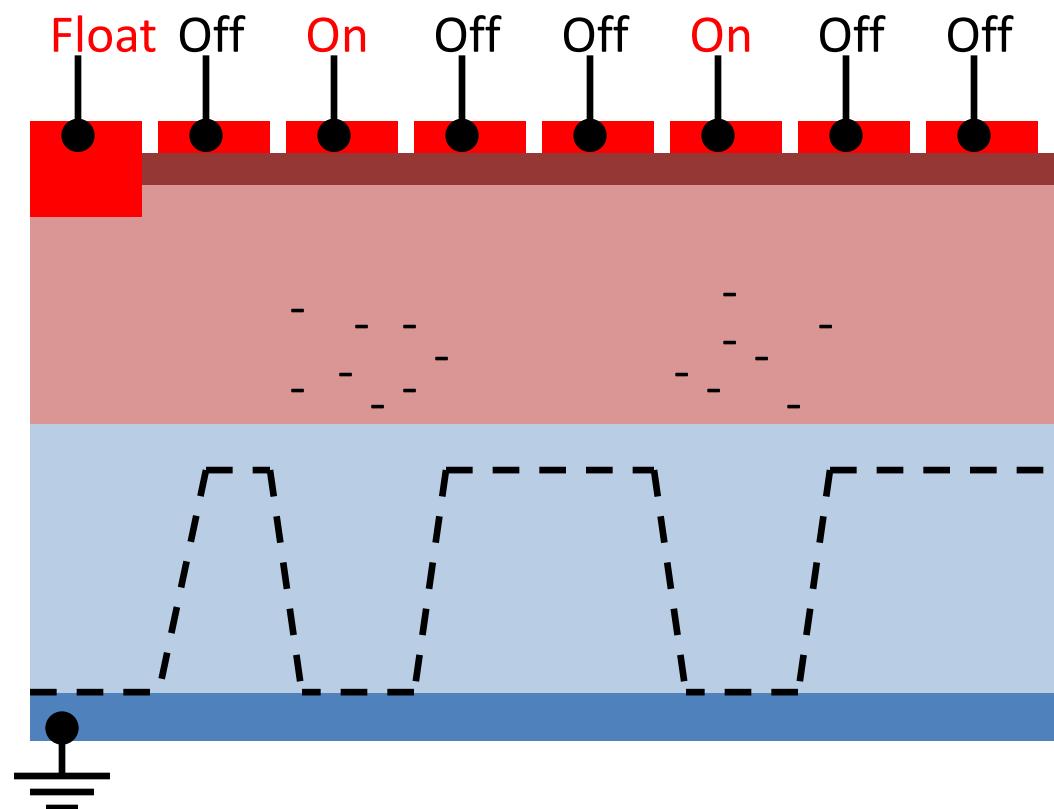
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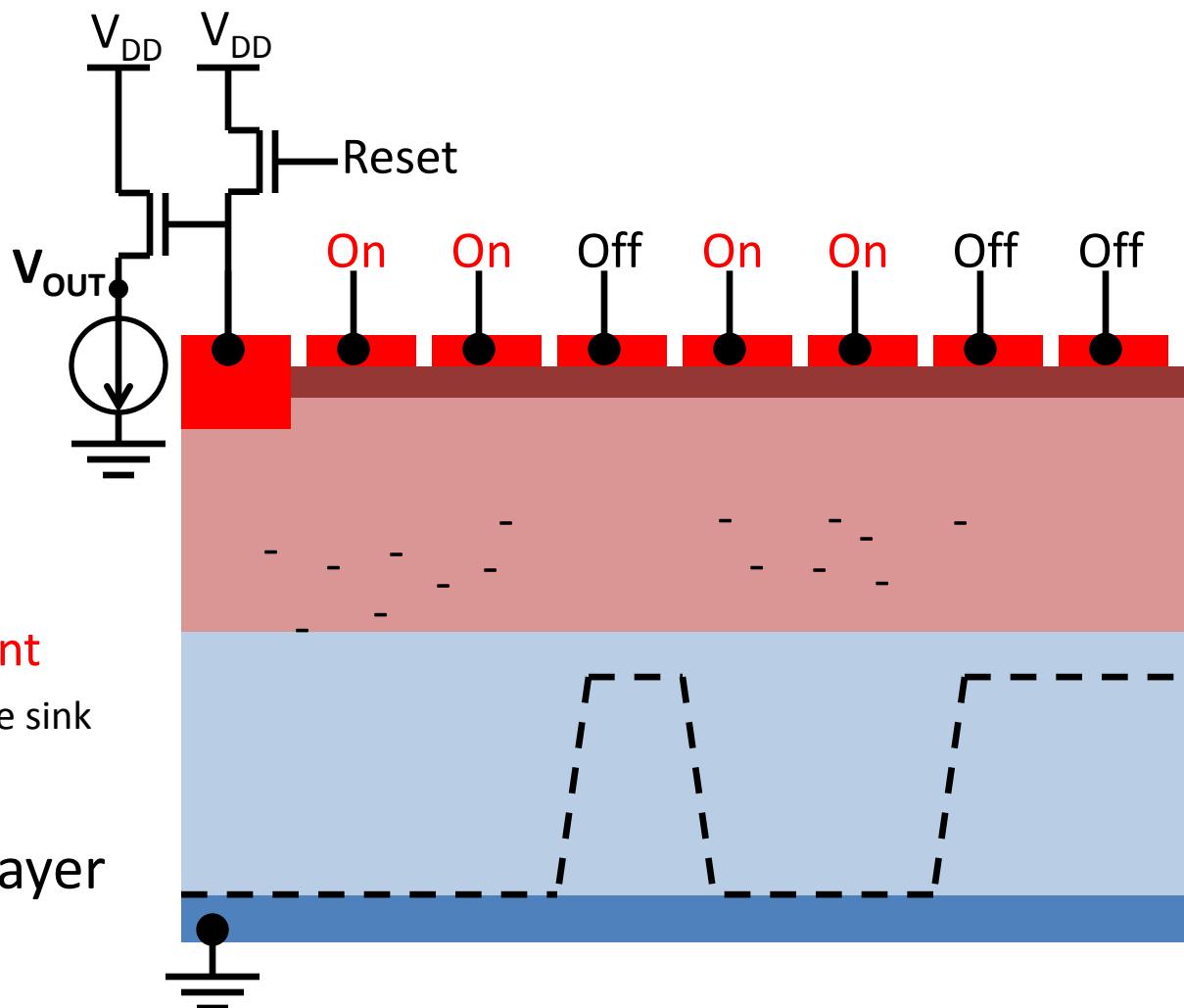
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CCD quick review

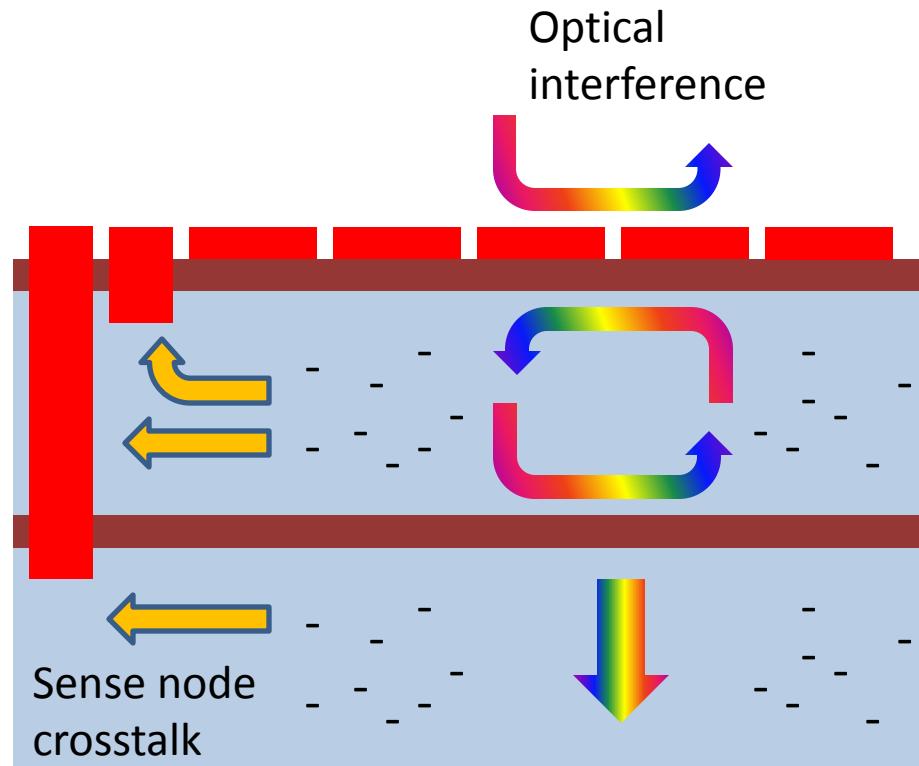
- Device operation
 - Equilibrium
 - Channel depletion
 - Optical absorption
 - Charge generation
 - Charge collection
 - Charge transfer
 - Charge measurement
 - Sense node \approx charge sink



Different for multi-layer
CCDs

Multi-layer SOI CCD

- Experimental demonstration
 - Optical absorption
 - Charge measurement
- Charge transfer

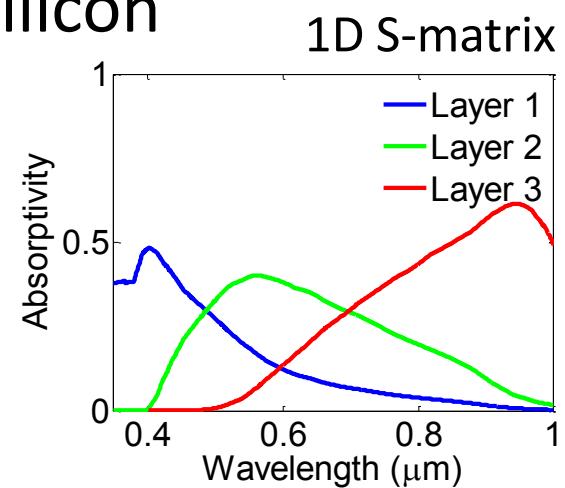
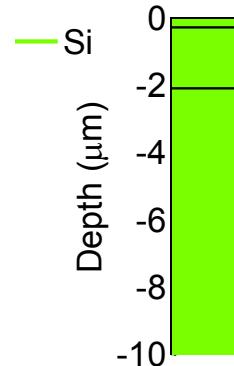


Optical design

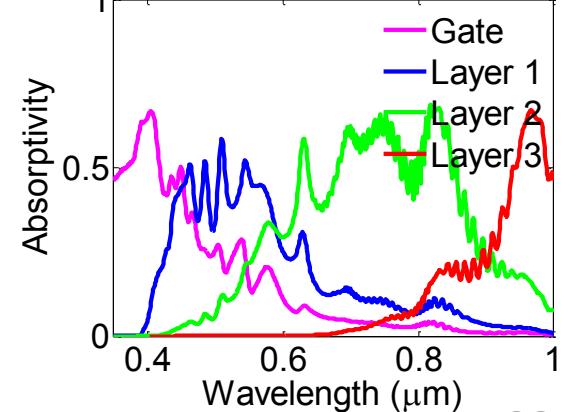
- Performance gain with simple designs
- Multi-layer dielectric for further improvements

— SiO_2
— Polysilicon
— Si

- Bare silicon

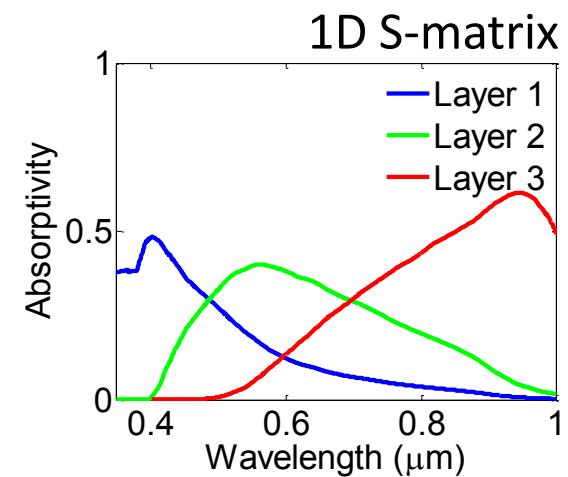
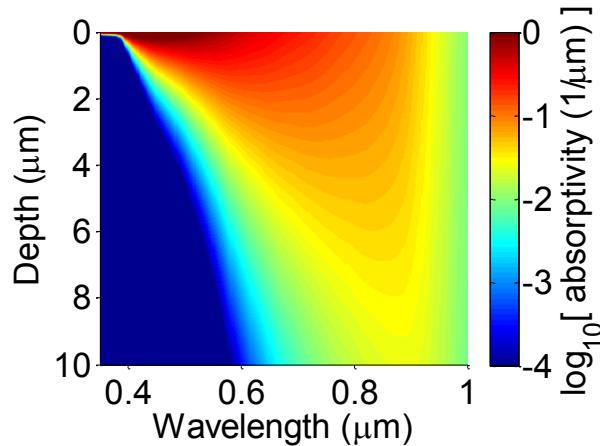
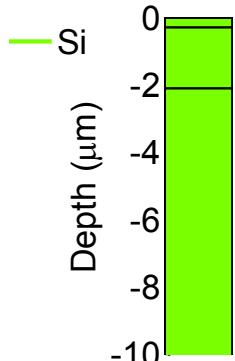


- 3-layer SOI

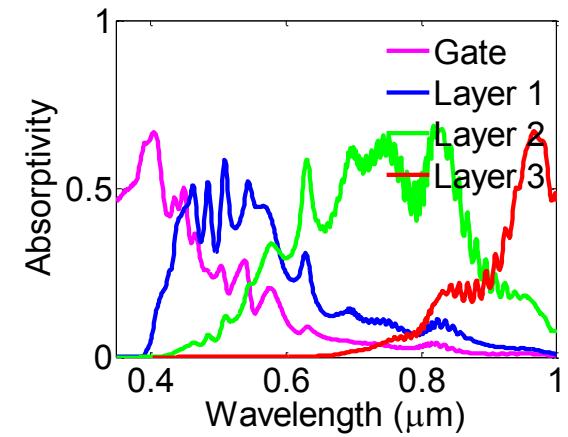
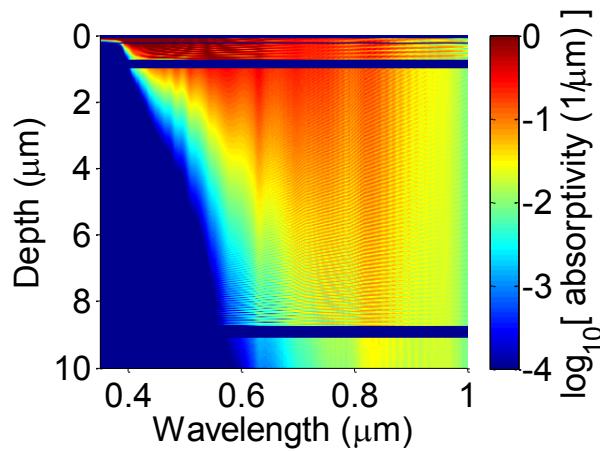
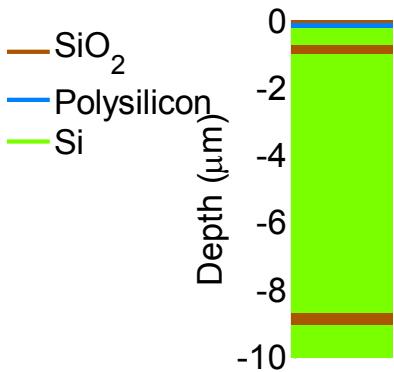


Optical design

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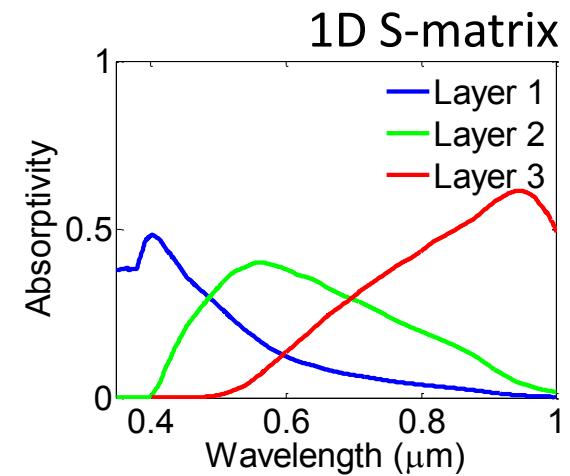
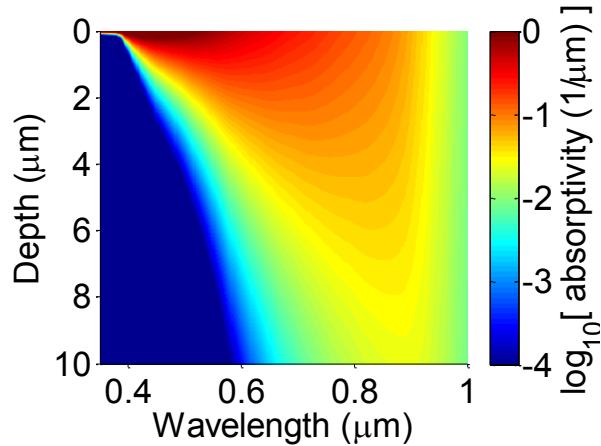
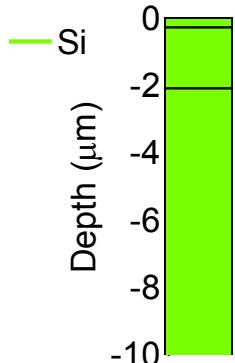


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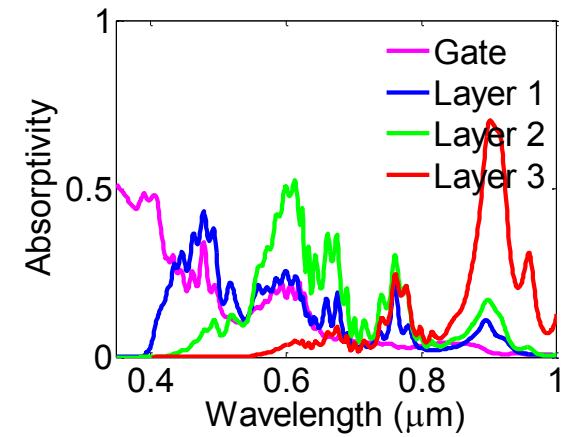
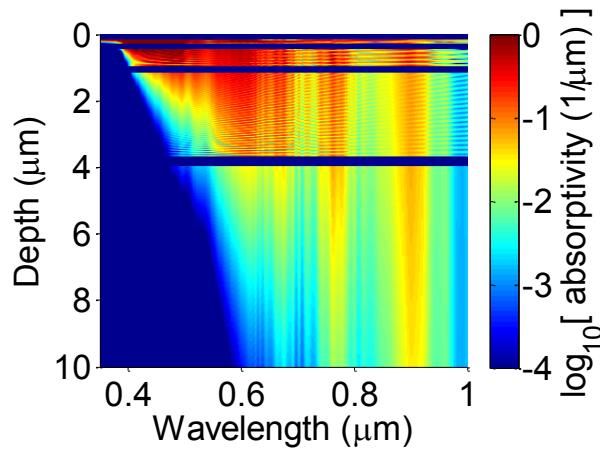
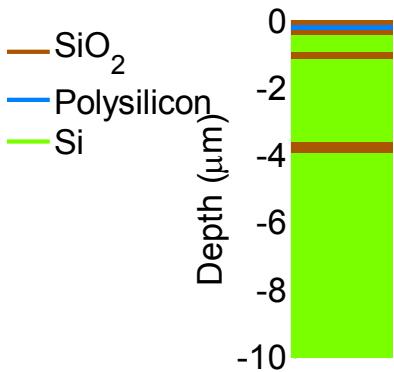


Optical design

- Bare silicon

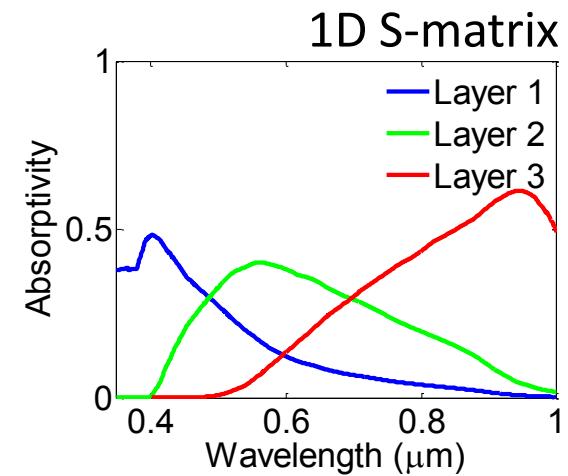
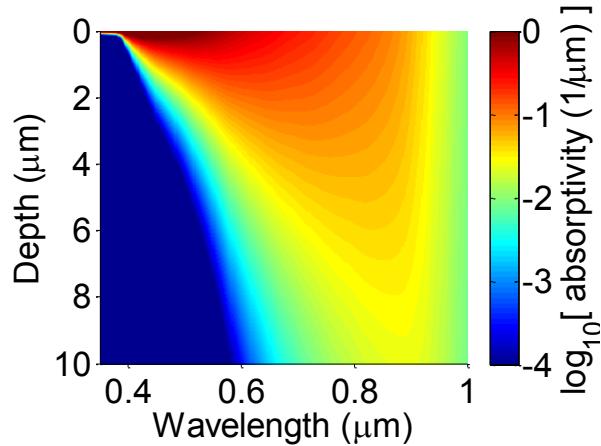
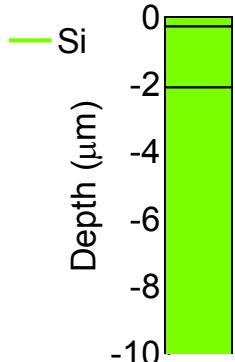


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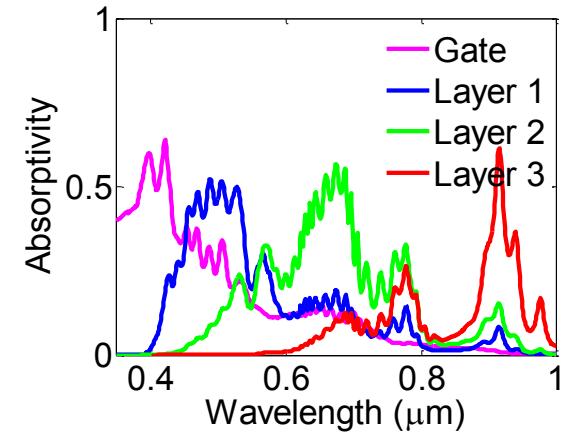
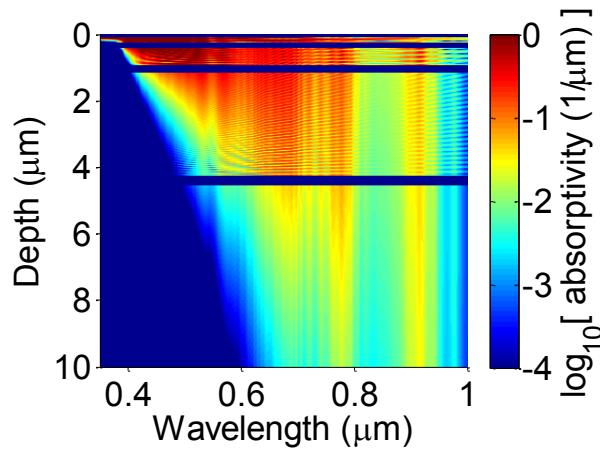
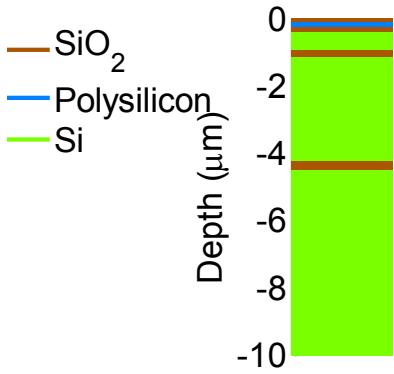


Optical design

- Bare silicon



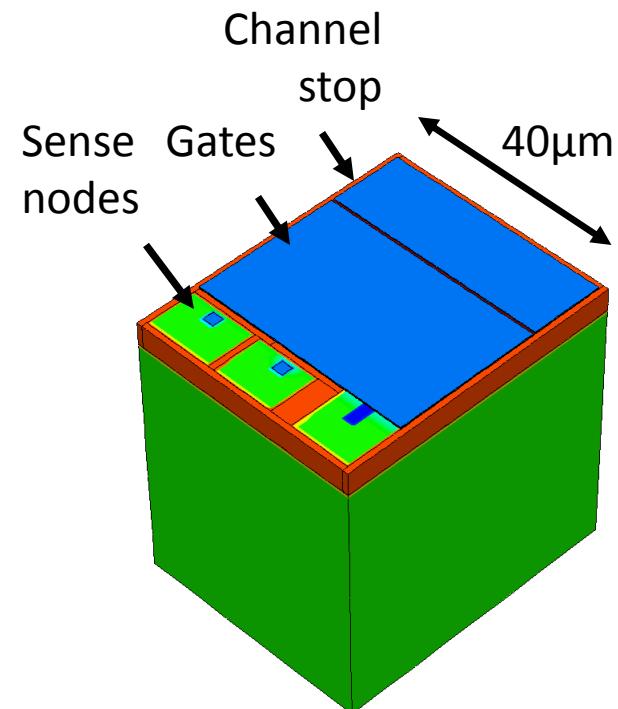
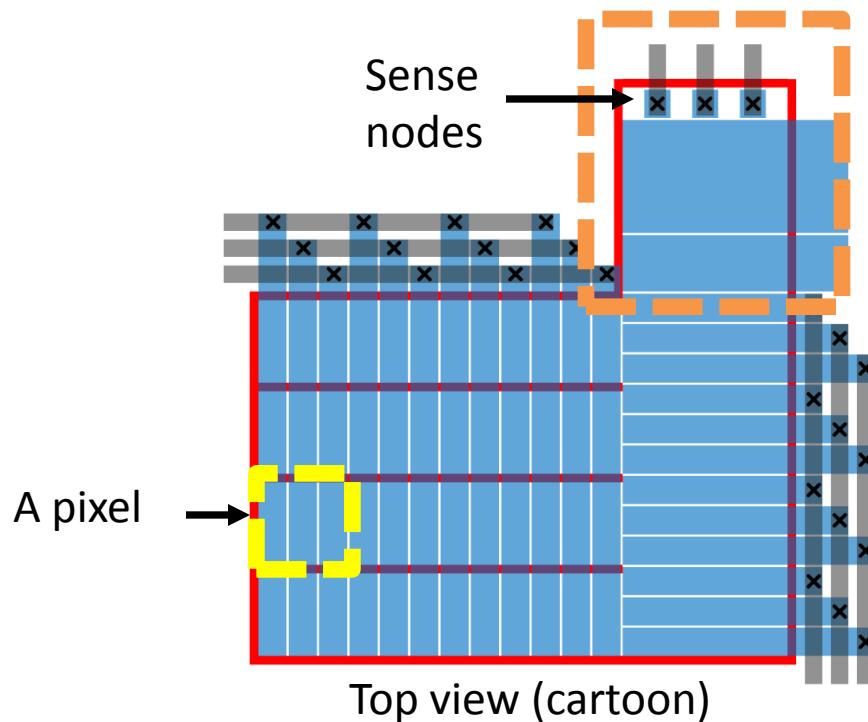
- 3-layer SOI



- Can we extract signals from these layers?

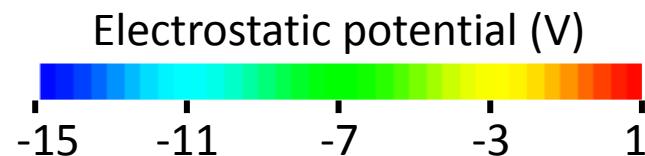
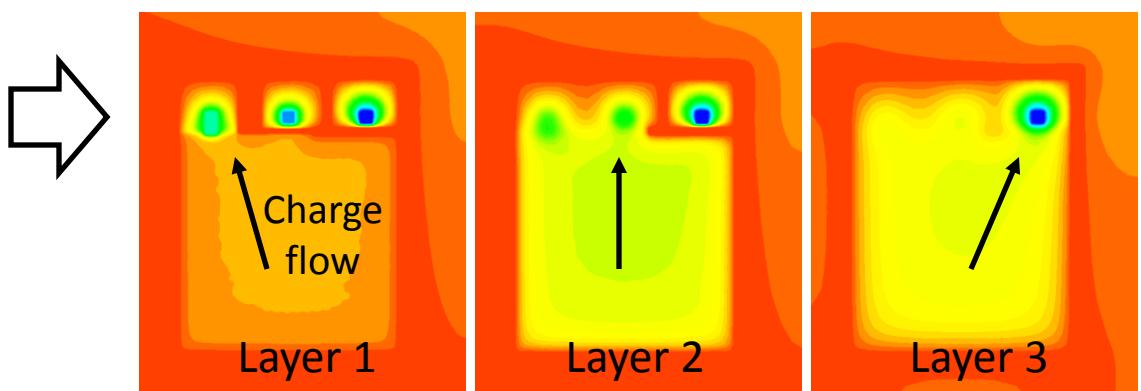
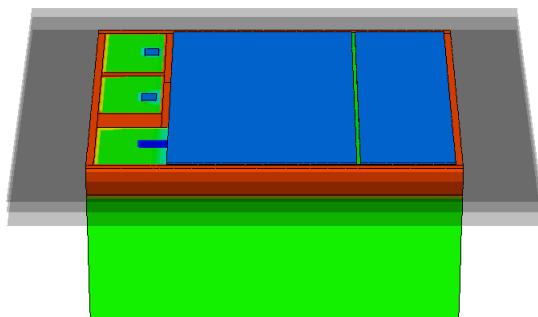
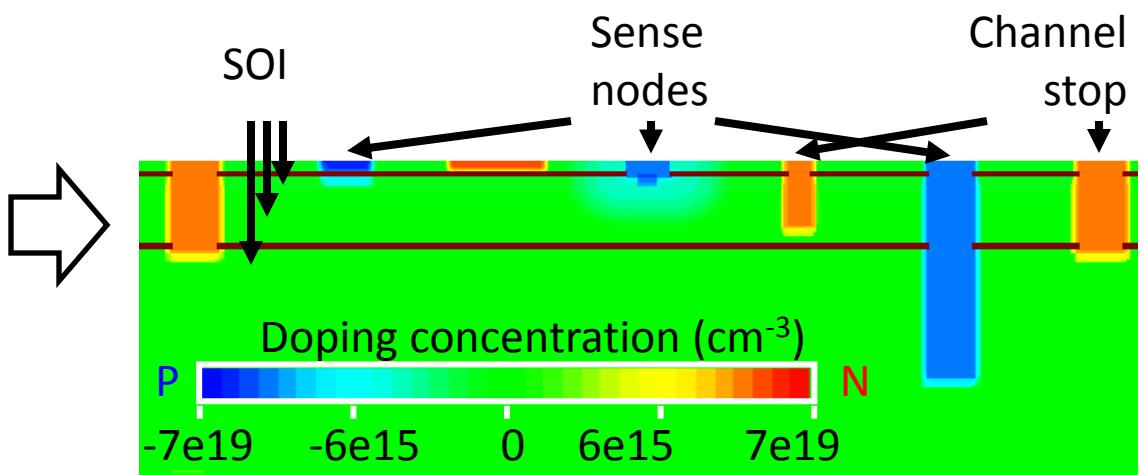
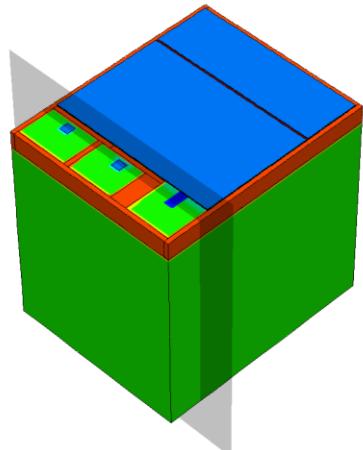
Device design

- Special sense nodes can extract charge with little crosstalk.

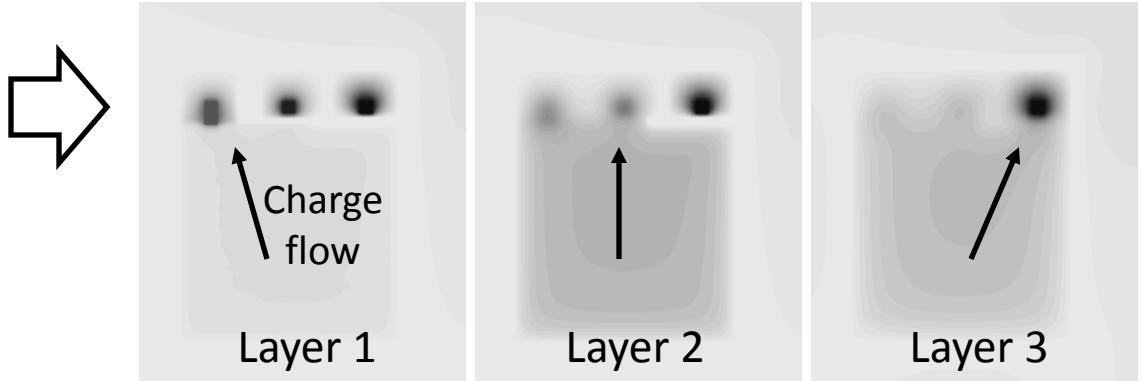
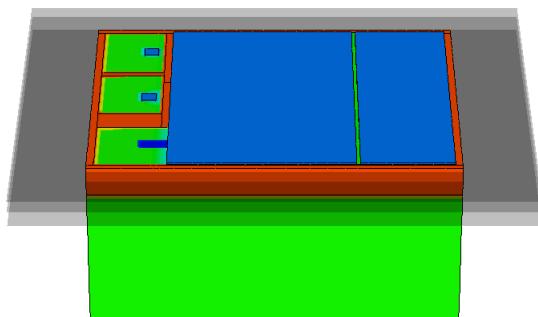
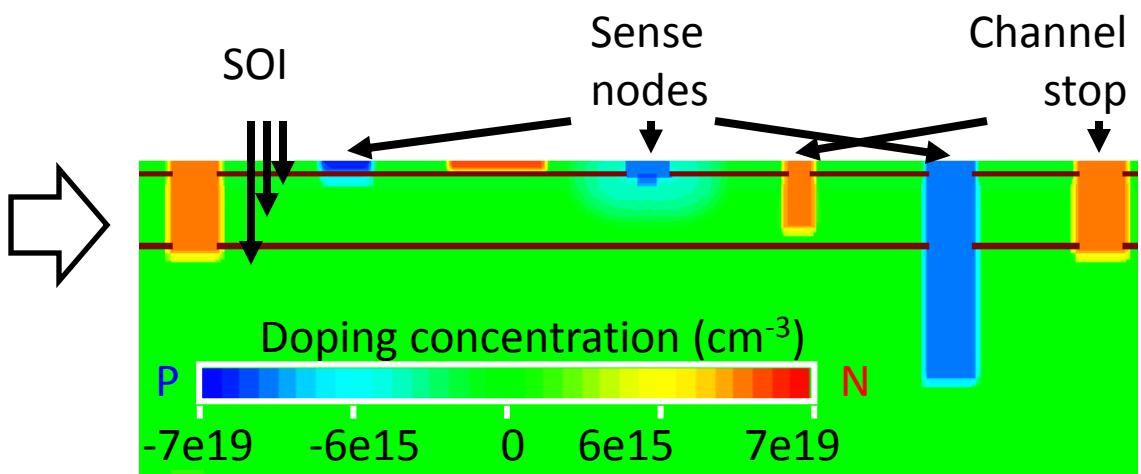
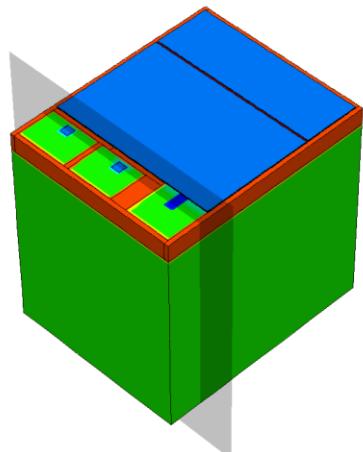


3D model in Synopsys
Sentaurus

Device design



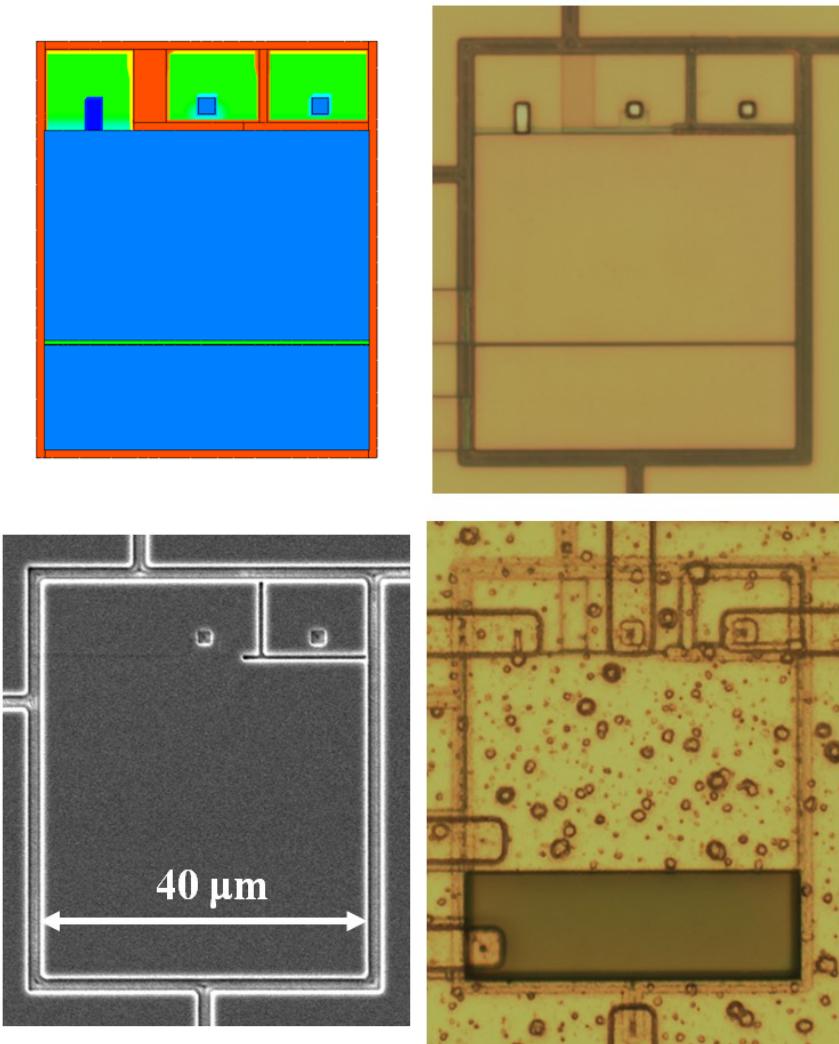
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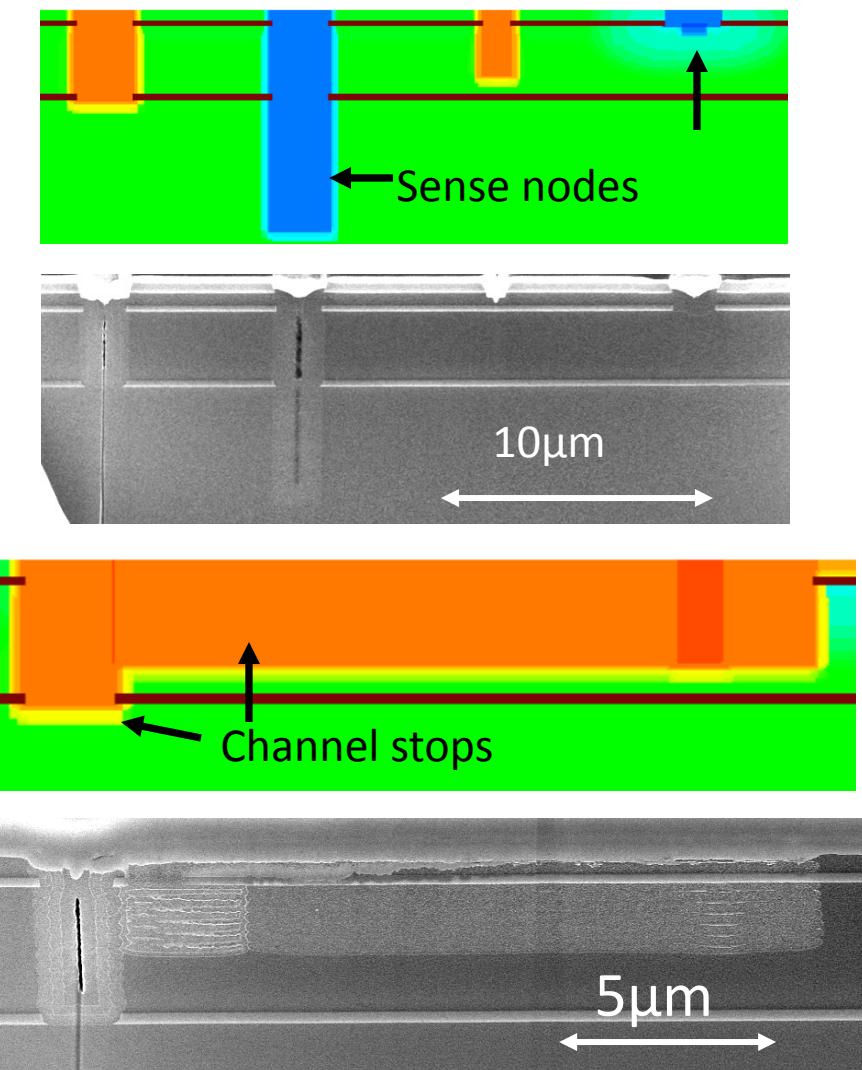
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- Future work

Experiment



Top views

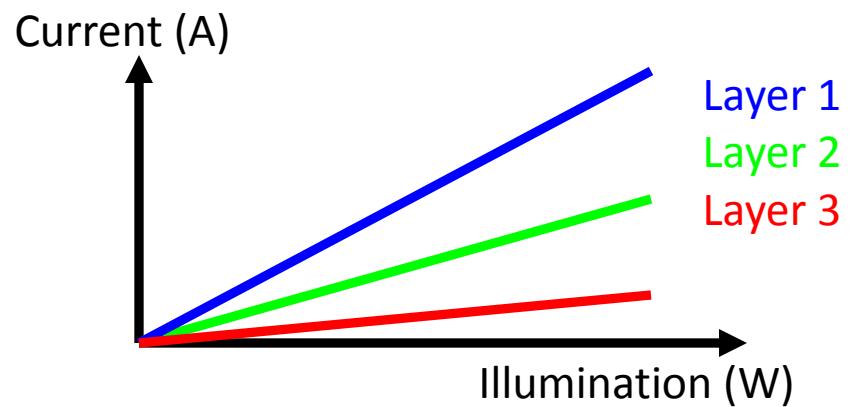
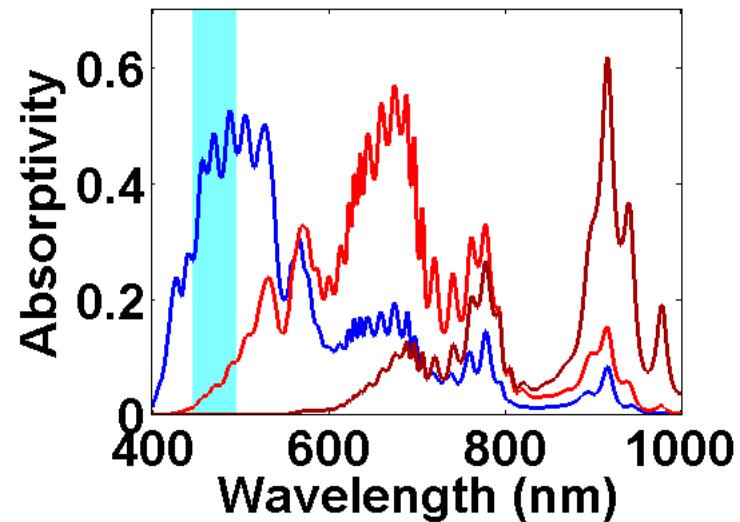
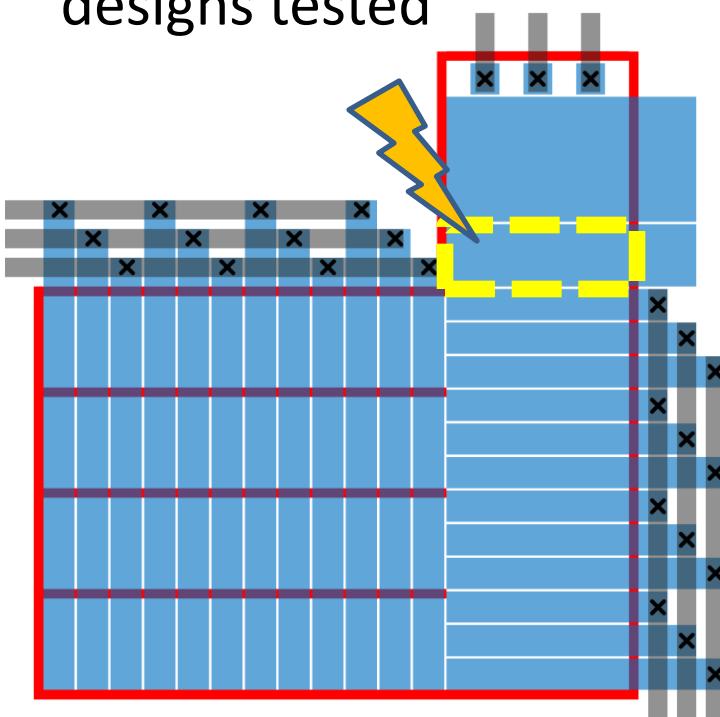


52° cross sections

30

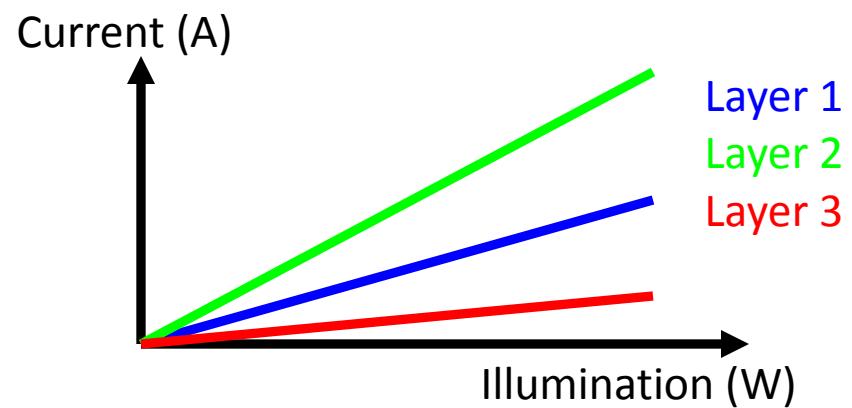
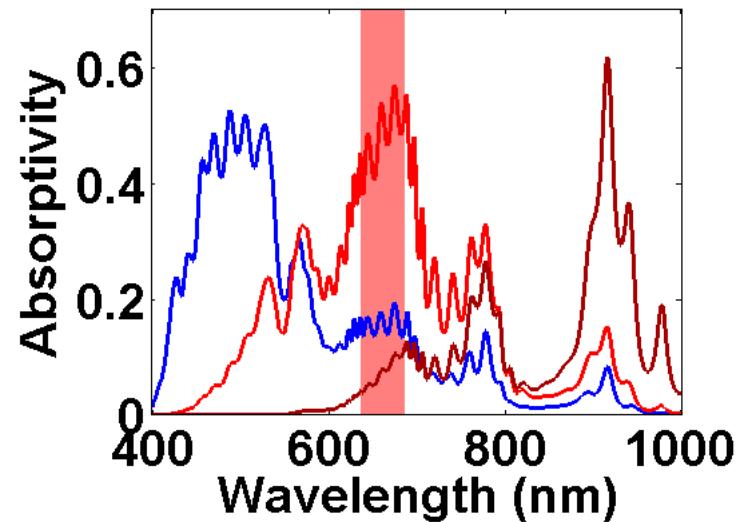
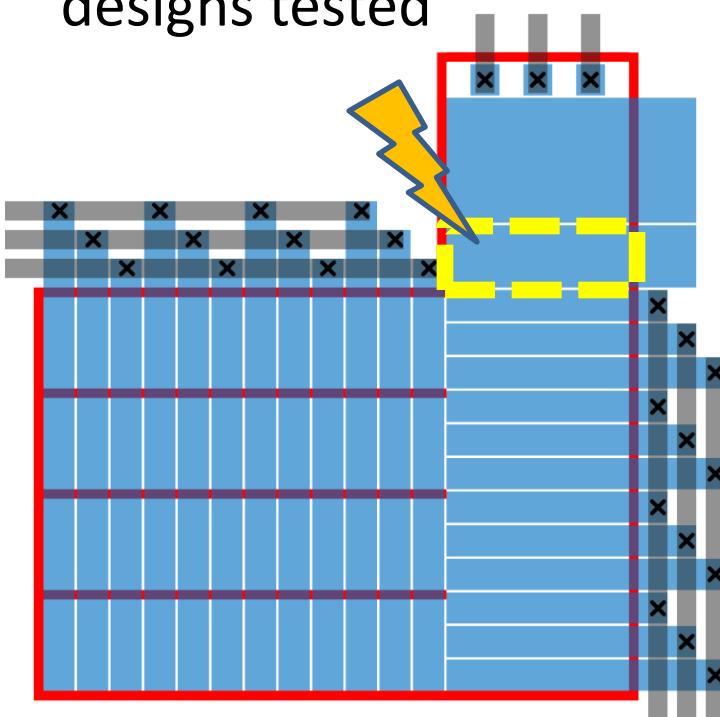
Experiment

- Constant light, DC bias
 - $\text{Current}_{\text{layer}}(\lambda)$
 - Optical and sense node designs tested



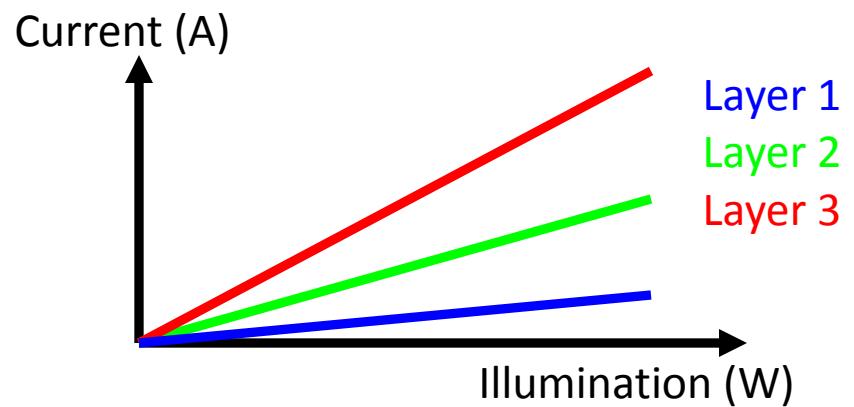
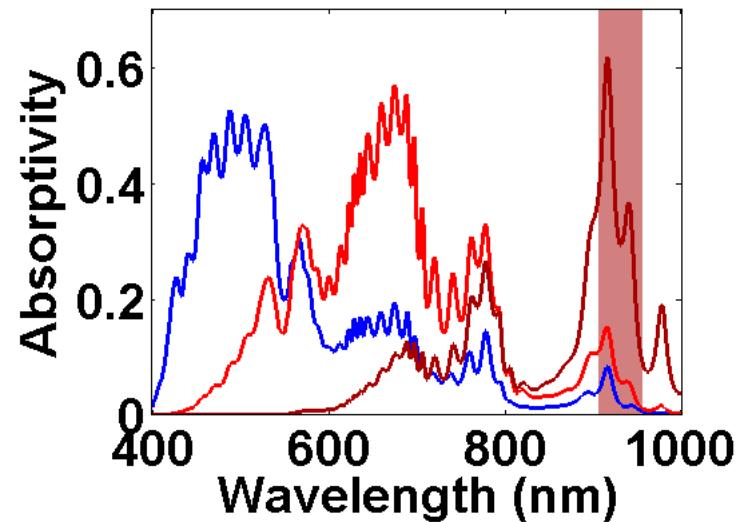
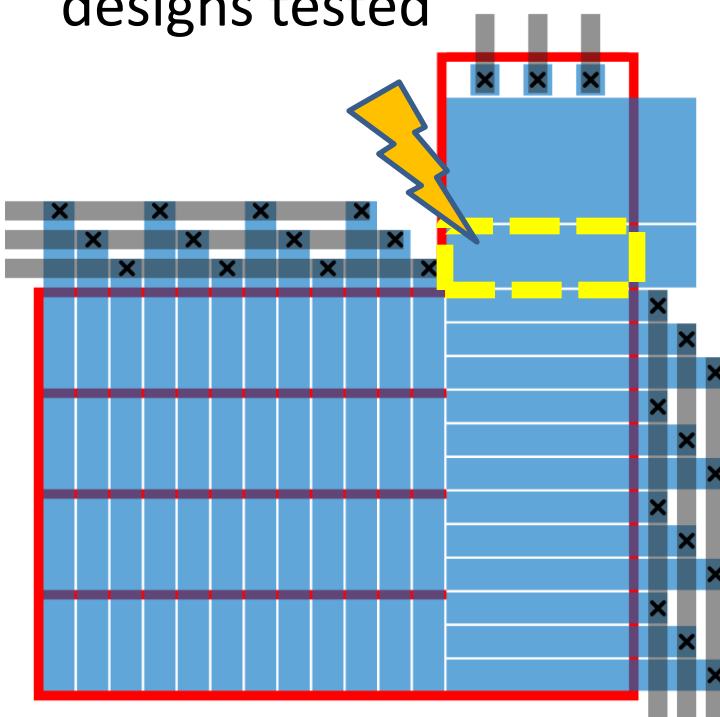
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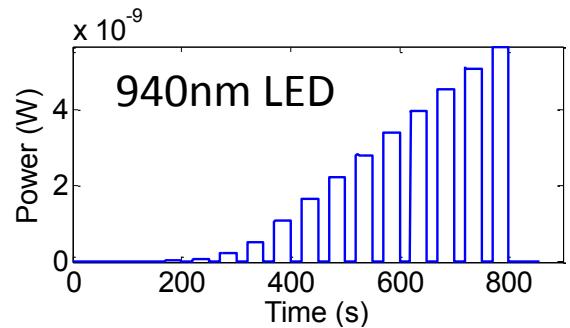
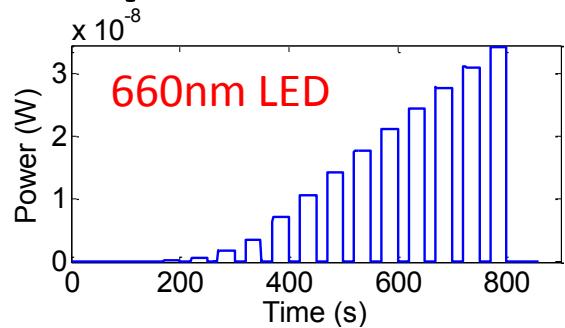
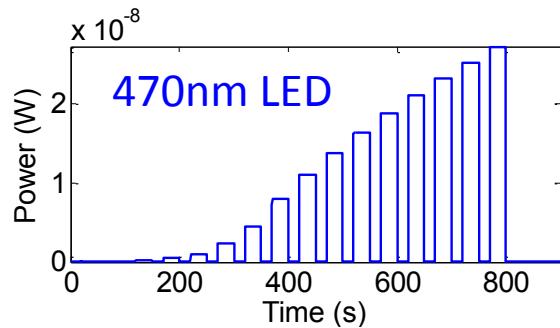


Experiment

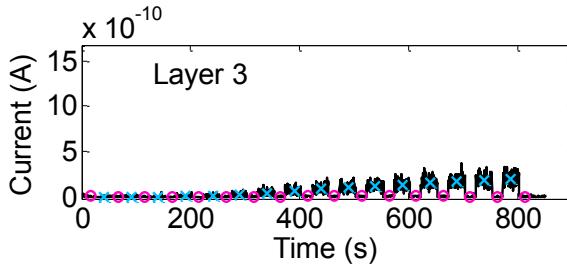
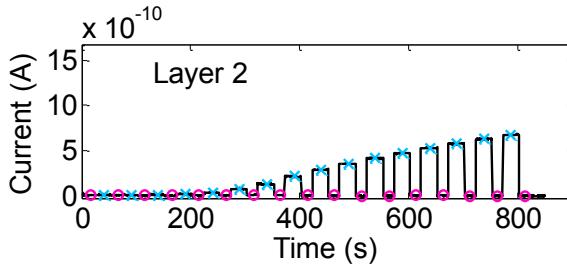
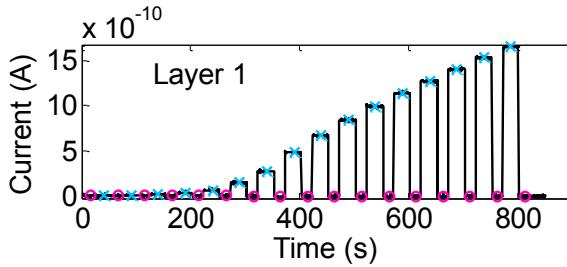
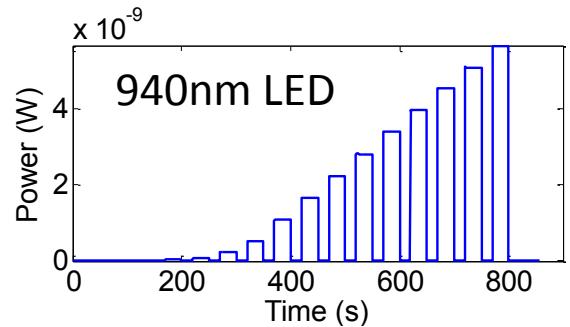
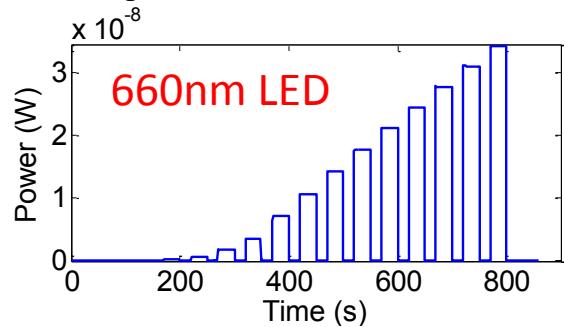
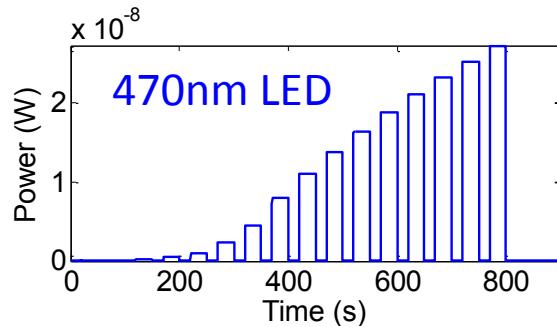
- Constant light, DC bias
 - $\text{Current}_{\text{layer}}(\lambda)$
 - Optical and sense node designs tested



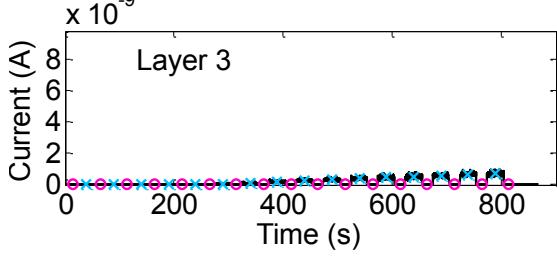
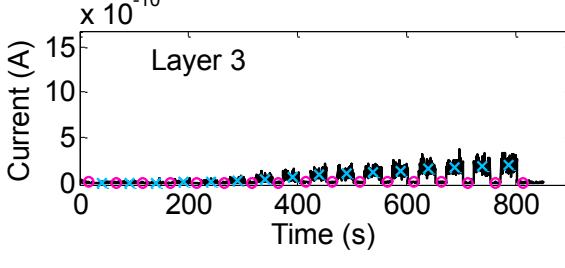
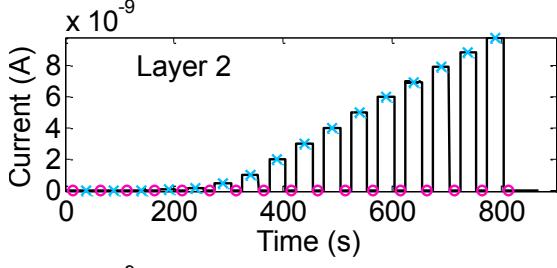
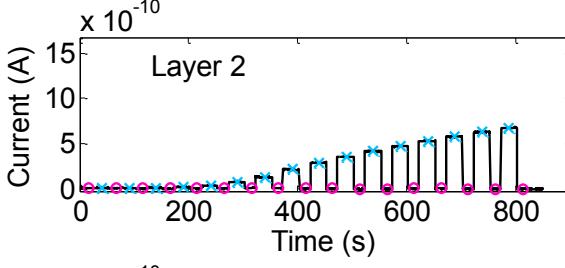
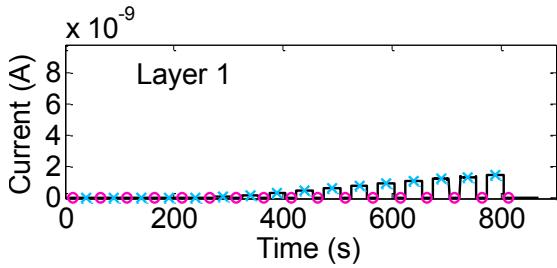
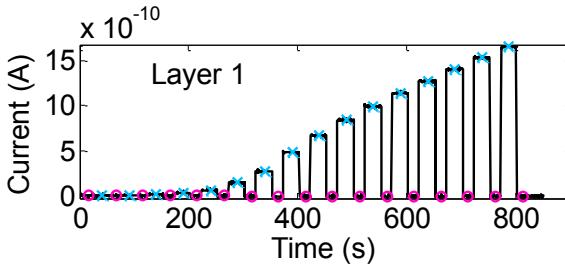
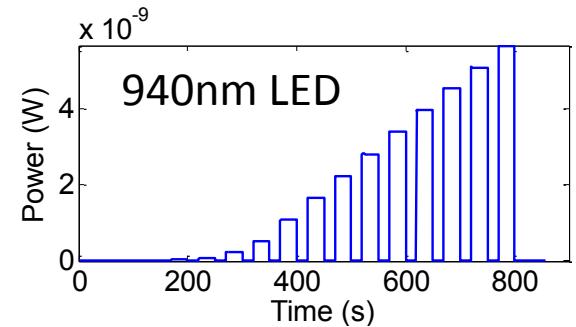
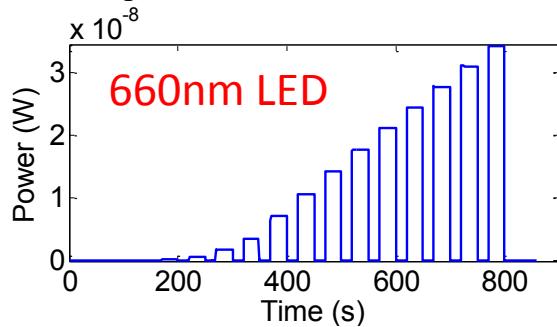
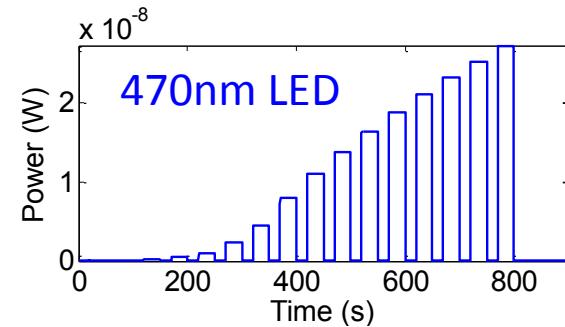
Experiment



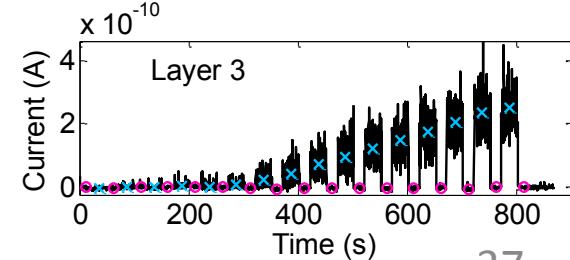
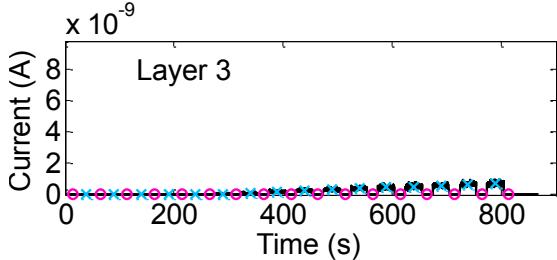
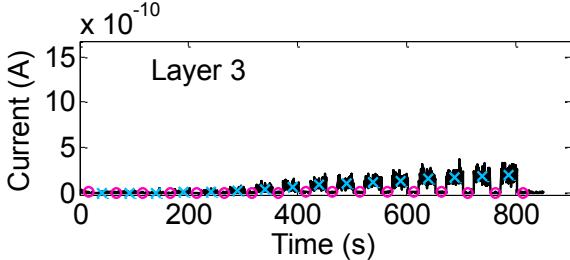
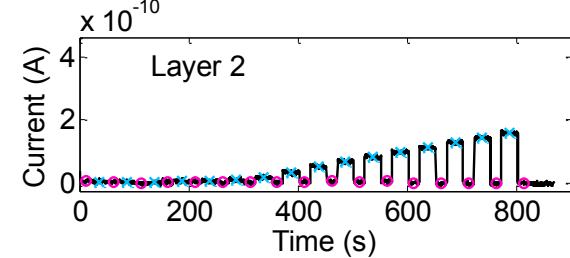
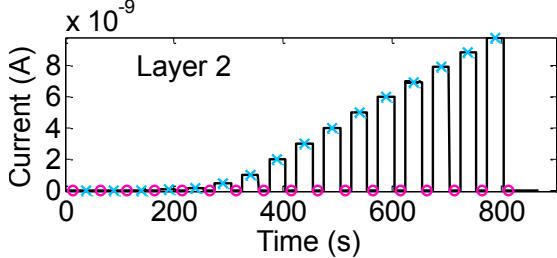
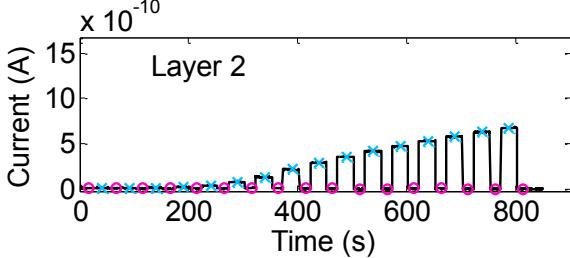
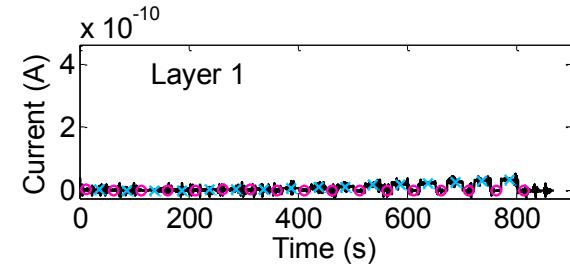
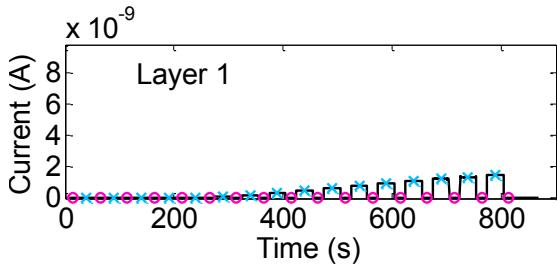
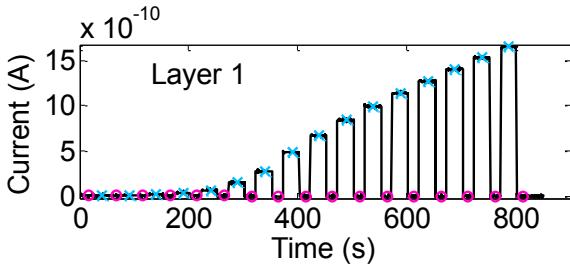
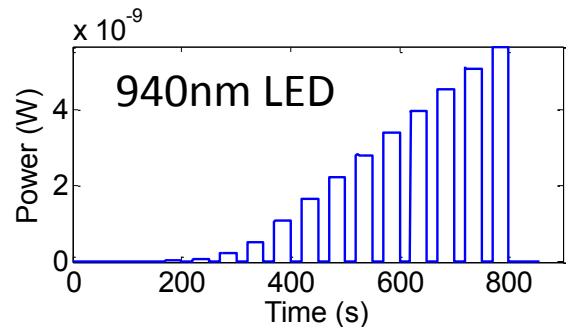
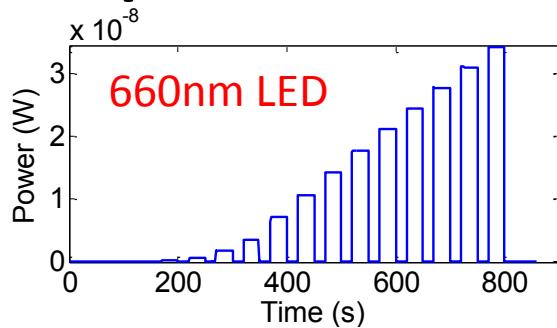
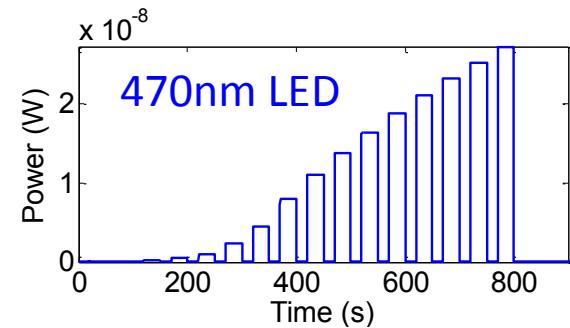
Experiment



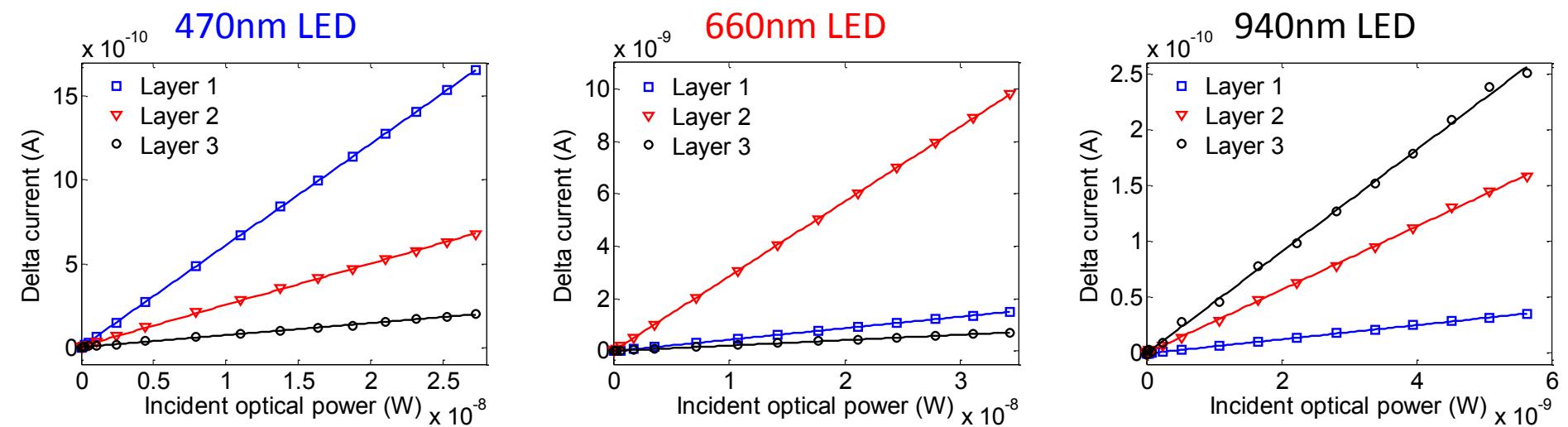
Experiment



Experiment

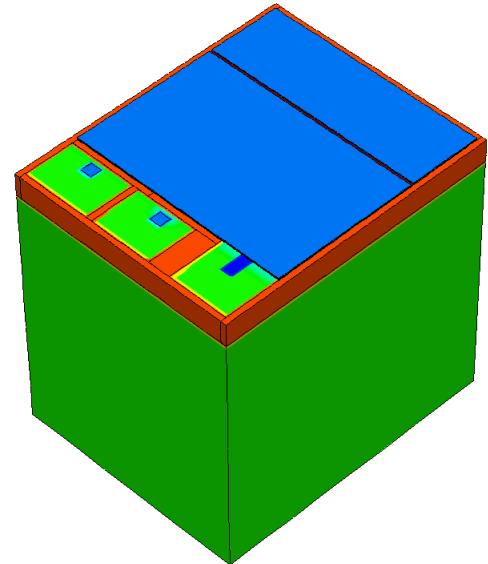
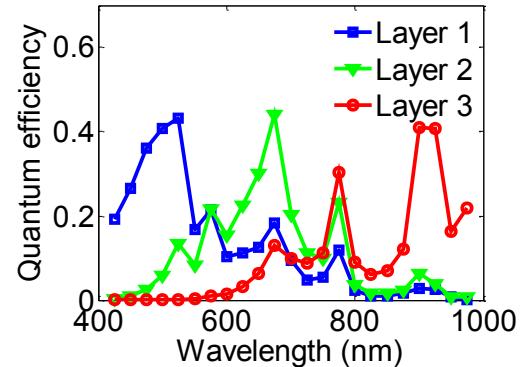


Experiment



Conclusion

- Multi-layer SOI CCD design
 - Optical and sense node designs
 - First experiment demo
- Future work
 - Charge transfer
 - Multi-layer dielectric
 - Performance analysis
 - Full well, linearity, CDS, noise



Acknowledgement



Stanford | ENGINEERING
Electrical Engineering



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Aaron Roodman



Julie Segal



Roger Howe